

Abstract**Transceiver**

A transceiver having an RF receiver, in particular VHF receiver, with digital signal processing in a digital part (DIT), with an input part, at least one mixer and an intermediate frequency/baseband processing means (BBV), having a local channel oscillator (EVO), to which a first phase locked loop with a phase discriminator (PD1) and an adjustable first frequency converter are assigned, and having a reference oscillator (REO) for the phase locked loop and the control clock (f_{st}) of the digital signal processing means, in which, in order to acquire the control clock (f_{ste}), a digital clock synthesizer (DDS) is provided to which an output signal (S_{REF}) of the reference oscillator (REO) is supplied, and a control signal (S_{AFC}) is supplied by the digital part (DIT) in the form of a digital tuning word, the frequency (f_{REO}) of the reference oscillator (REO) being selected such that its order of magnitude is at least equal to the bandwidth of one or more of the reception bands used, and none of its harmonics occur in a reception channel.

Fig. 1

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(54) Title: TRANSMITTER-RECEIVER

(54) Bezeichnung: SENDEEMPFÄNGER

(57) Abstract

